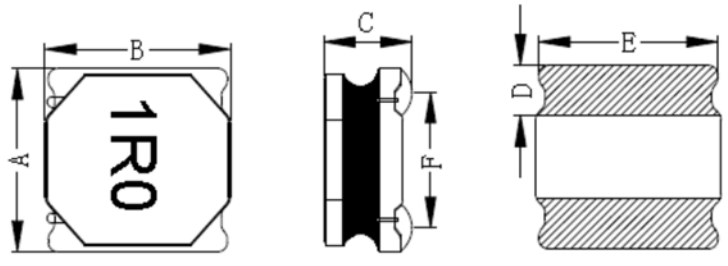
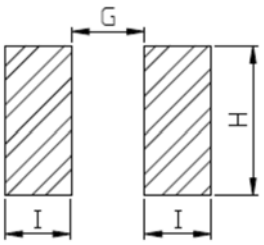
FEATRLRES

- This specification applies Low Profile Power Inductors.
- 100% Lead(Pb) & Halogen-Free and RoHS compliant.

CONFIGRLRATIONS & DIMENSIONS (unit in mm)



Recommended Land pattern



Type	A	B	C	D	E	F
HNR6028NF	6.0±0.2	6.0±0.2	2.6±0.2	1.6±0.3	5.8±0.3	4.3ref

G	H	I
2.5	5.8	1.8

*Dimensions are not including the termination. For maximum overall dimensions with termination , add 0.1mm

Note:
1. The above PCB layout reference only.
2. Recommend solder paste thickness at 0.12mm and above.

ELECTRICAL CHARACTERISTICS

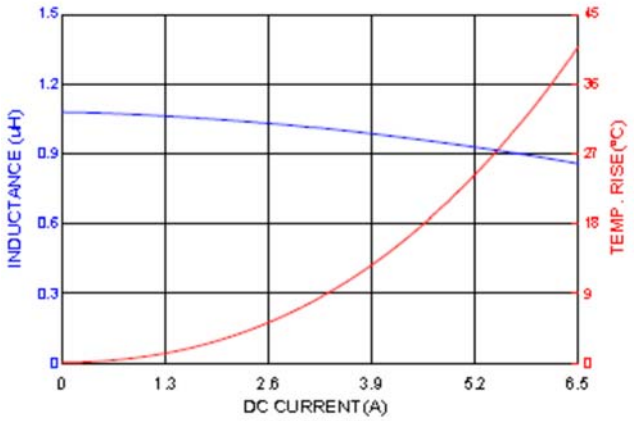
Part Number	Inductance L0 (uH)±20% @ 0 A	Rated current		DCR (mΩ) @25℃ ±20%.
		Temperature current I rms (A)	Saturation current I sat (A)	
HNR6028NF-1R0Y	1.00±30%	5.20	5.75	10.0
HNR6028NF-1R5Y	1.50±30%	4.95	5.30	14.0
HNR6028NF-2R2M	2.20±20%	4.50	5.00	18.0
HNR6028NF-3R3M	3.30±20%	3.60	4.30	24.0
HNR6028NF-4R7M	4.70±20%	3.10	3.20	30.0
HNR6028NF-6R8M	6.80±20%	2.50	2.85	47.0
HNR6028NF-100M	10.0±20%	2.00	2.10	65.0
HNR6028NF-150M	15.0±20%	1.80	2.00	98.0
HNR6028NF-220M	22.0±20%	1.50	1.60	138
HNR6028NF-330M	33.0±20%	1.30	1.40	200
HNR6028NF-470M	47.0±20%	1.06	1.15	280
HNR6028NF-680M	68.0±20%	0.81	1.00	420
HNR6028NF-101M	100±20%	0.72	0.80	605

Note:
1.All test data referenced to 25℃ ambient , Ls:100KHz/1V.

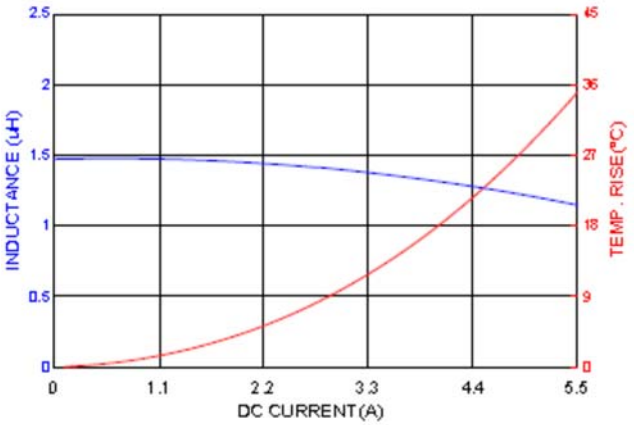
2.Testing Instrument : HP4284A,CH11025,CH3302,CH1320 ,CH1320S LCR METER / Rdc:CH502BC MICRO OHMMETER.
3.Heat Rated Current (Irms) will cause the coil temperature rise approximately △t of 40℃.
4.Saturation Current (Isat) will cause L0 to drop approximately 30%.
5.The part temperature (ambient + temp rise) should not exceed 125℃under worst case operating conditions.Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
6.Special inquiries besides the above common used types can be met on your requirement.

TYPICALELECTRICALCHARACTERISTICS:

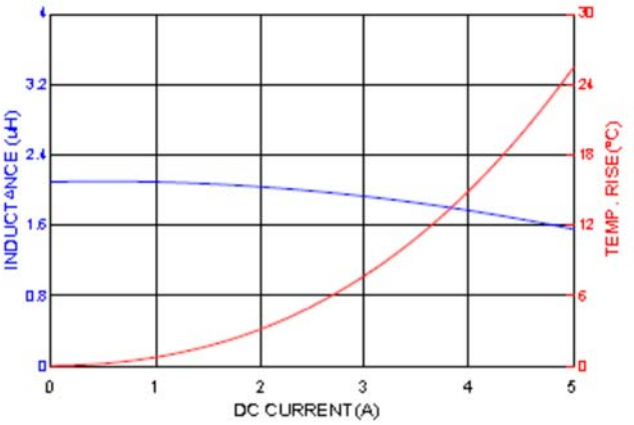
HNR6028NF-1R0



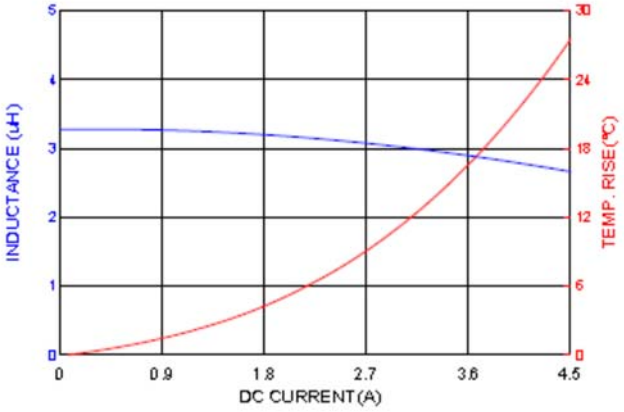
HNR6028NF-1R5



HNR6028NF-2R2



HNR6028NF-3R3



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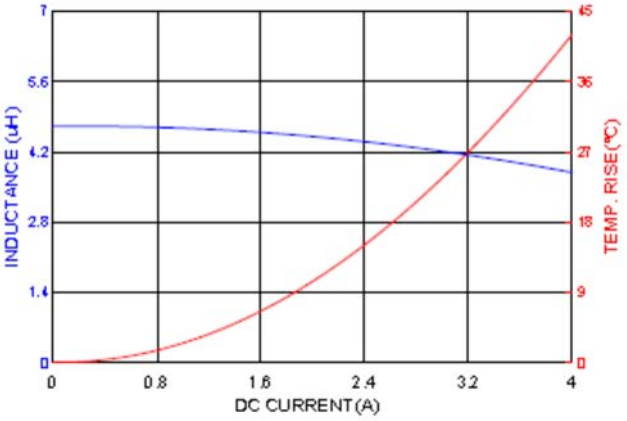
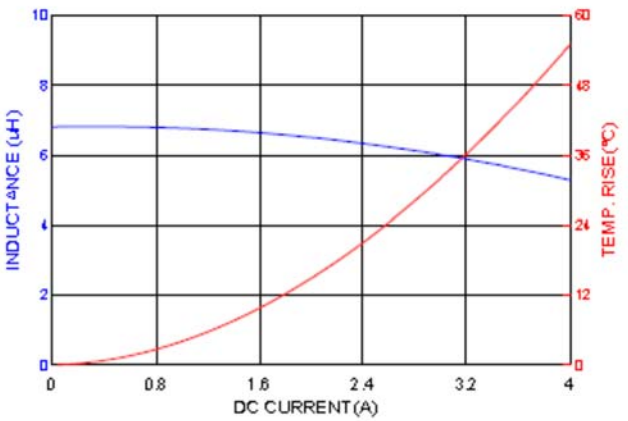
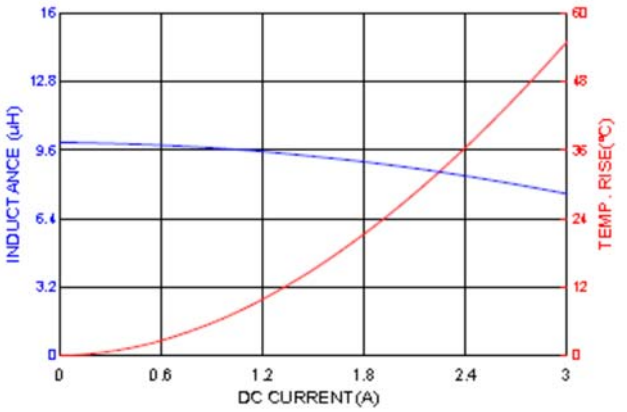


HNR6028NF-6R8



HNR6028NF-100



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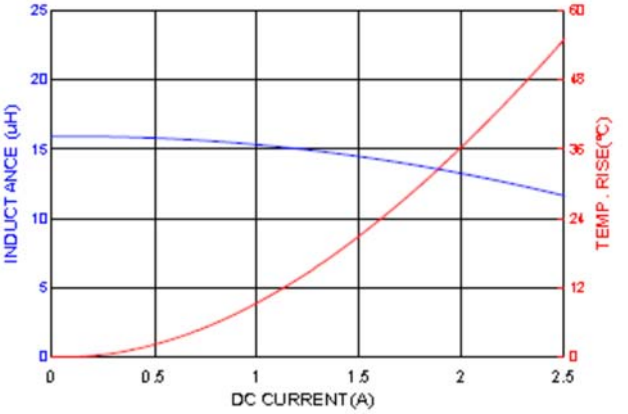
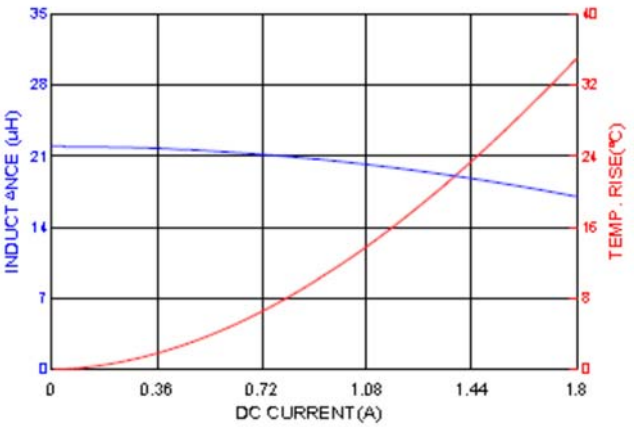
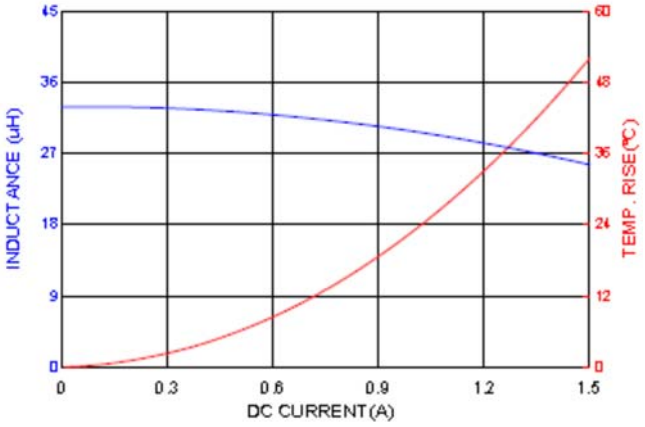


HNR6028NF-220



HNR6028NF-330



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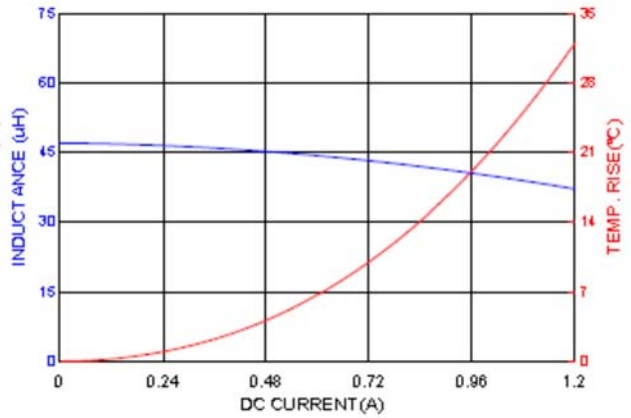

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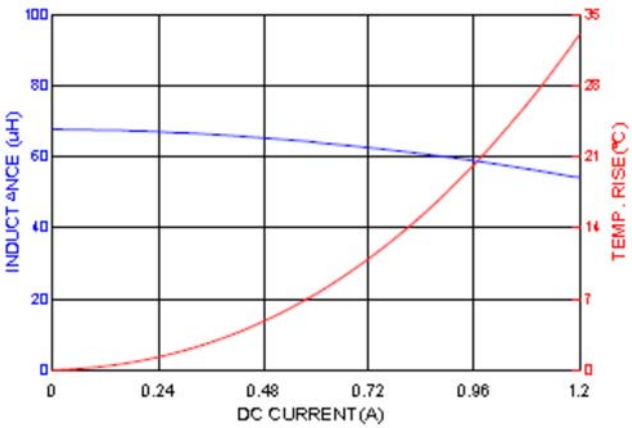
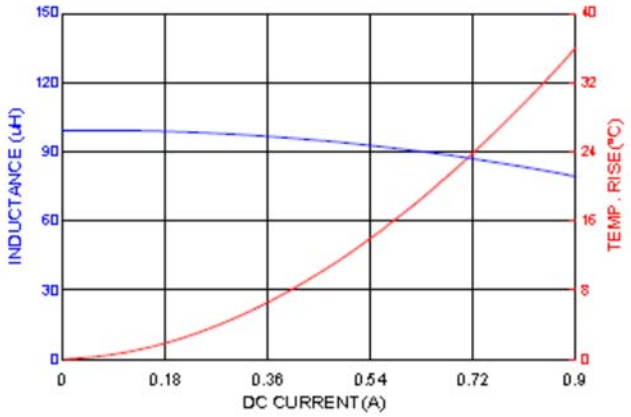
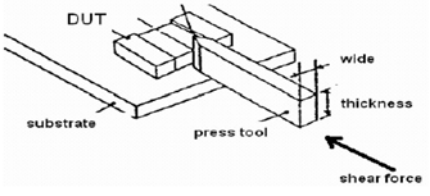


HNR6028NF-101



Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	1. -10~+40℃,50~60%RH (Product with taping) 2. -40~+125℃(on board)	
Electrical Performance Test		
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately△L30%	Saturation DC Current (Isat) will cause L0 to drop △L(%)
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance：No damage.	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature： 125±2℃(Inductor) Applied current：rated current Duration：1000±12hrs Measured at room temperature after placing for 24±2 hrs

<table>
<tr><td>Load Humidity</td><td rowspan="4">Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles
Humidity：85±2 % R.H,
Temperature：85℃±2℃
Duration：1000hrs Min. with 100% rated current
Measured at room temperature after placing for 24±2 hrs</td></tr>
<tr><td>Moisture Resistance</td><td>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow
Profiles
1. Baked at50℃ for 25hrs, measured at room temperature after placing for 4 hrs.
2. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs.
3. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in
2.5hrs,keep at 25℃ for 2 hrs then keep at -10℃ for 3 hrs
4. Keep at 25℃ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at
room temperature after placing for 1~2 hrs.</td></tr>
<tr><td>Thermal
shock</td><td>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification
Reflow Profiles
Condition for 1 cycle
Step1：-40±2℃ 30±5min
Step2：25±2℃ ≦0.5min
Step3：125±2℃ 30±5min
Number of cycles： 500
Measured at room temperature after placing for 24±2 hrs</td></tr>
<tr><td>Vibration</td><td>Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes
Equipment： Vibration checker
Total Amplitude:1.52mm±10%
Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)。</td></tr>
<tr><td>Bending</td><td rowspan="2">Appearance：No damage.
Impedance：within±15% of initial value
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm
<0805 inch(2012mm):40x100x0.8mm
Bending depth: >=0805 inch(2012mm):1.2mm
<0805 inch(2012mm):0.8mm
duration of 10 sec.</td></tr>
<tr><td>Shock</td><td>
<table>
<tr><th>Type</th><th>Peak value (g's)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (Vi)ft/sec</th></tr>
<tr><td>SMD</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr>
<tr><td>Lead</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr>
</table>
</td></tr>
<tr><td>Solder ability</td><td>More than 95% of the terminal electrode should be covered with solder。</td><td>Preheat: 150℃,60sec.。
Solder: Sn96.5% Ag3% Cu0.5%
Temperature: 245±5℃。
Flux for lead free: Rosin. 9.5%。
Dip time: 4±1sec。
Depth: completely cover the termination</td></tr>
<tr><td>Resistance to Soldering Heat</td><td rowspan="2">Appearance：No damage.
Impedance：within±15% of initial value
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value e</td><td>Depth: completely cover the termination
<table>
<tr><th>Temperature(°C)</th><th>Time(s)</th><th>Temperature ramp/immersion and emersion rate</th><th>Number of heat cycles</th></tr>
<tr><td>260 ±5 (solder temp)</td><td>10 ±1</td><td>25mm/s ±6 mm/s</td><td>1</td></tr>
</table>
</td></tr>
<tr><td>Terminal
Strength</td><td>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles
With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.
 
</td></tr>
</table>

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.